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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO
10/827,409	04/19/2004	Robert Louis Hodges	98-P-104D1 (850063.542D1)	2598
30423 7	590 09/28/2005		EXAMINER	
STMICROEL	LECTRONICS, INC.		SOWARD	, IDA M
MAIL STATIO	ON 2346 ONICS DRIVE		ART UNIT	PAPER NUMBER
	N, TX 75006		2822	

DATE MAILED: 09/28/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

			11.1
	Application No.	Applicant(s)	-
	10/827,409	HODGES, ROBERT LOU	IS
Office Action Summary	Examiner	Art Unit	
	Ida M. Soward	2822	
The MAILING DATE of this communication a Period for Reply	ppears on the cover sheet with the o	correspondence address	
A SHORTENED STATUTORY PERIOD FOR REP WHICHEVER IS LONGER, FROM THE MAILING  - Extensions of time may be available under the provisions of 37 CFR after SIX (6) MONTHS from the mailing date of this communication.  - If NO period for reply is specified above, the maximum statutory perior.  - Failure to reply within the set or extended period for reply will, by state Any reply received by the Office later than three months after the main earned patent term adjustment. See 37 CFR 1.704(b).	DATE OF THIS COMMUNICATION  1.136(a). In no event, however, may a reply be tind will apply and will expire SIX (6) MONTHS from the cause the application to become ABANDONE	N. mely filed the mailing date of this communicat () (35 U.S.C. § 133).	
Status			
1) Responsive to communication(s) filed on 19	April 2004.		
2a) This action is <b>FINAL</b> . 2b) Th	nis action is non-final.		
3) Since this application is in condition for allow		•	is
closed in accordance with the practice under	Ex parte Quayle, 1935 C.D. 11, 4	53 O.G. 213.	
Disposition of Claims			
4)⊠ Claim(s) <u>1-19</u> is/are pending in the application	on.		
4a) Of the above claim(s) is/are withdo			4
5) Claim(s) is/are allowed.			
6) Claim(s) is/are rejected.			
7) Claim(s) is/are objected to.			
8) Claim(s) <u>1-19</u> are subject to restriction and/o	r election requirement.		
Application Papers			
9) The specification is objected to by the Exami	ner.	•	
10) The drawing(s) filed on is/are: a) a	ccepted or b) objected to by the	Examiner.	
Applicant may not request that any objection to the	ne drawing(s) be held in abeyance. Se	e 37 CFR 1.85(a).	
Replacement drawing sheet(s) including the corre			
11) The oath or declaration is objected to by the	Examiner. Note the attached Office	e Action or form PTO-152.	
Priority under 35 U.S.C. § 119			
12) Acknowledgment is made of a claim for foreignal All b) Some * c) None of:	gn priority under 35 U.S.C. § 119(a	a)-(d) or (f).	
1. ☐ Certified copies of the priority docume	ents have been received.		
2.☐ Certified copies of the priority docume		tion No	
3.☐ Copies of the certified copies of the pr			
application from the International Bure	eau (PCT Rule 17.2(a)).		
* See the attached detailed Office action for a li	st of the certified copies not receive	ed.	
Attachment(s)		·	
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948)	4) 🔲 Interview Summar Paper No(s)/Mail D		
<ol> <li>Notice of Draftsperson's Patent Drawing Review (PTO-948)</li> <li>Information Disclosure Statement(s) (PTO-1449 or PTO/SB/OPAPER No(s)/Mail Date</li> </ol>	5	Patent Application (PTO-152)	

Art Unit: 2822

## **DETAILED ACTION**

## Election/Restrictions

Restriction to one of the following inventions is required under 35 U.S.C. 121:

- Claims 14-19, drawn to a semiconductor structure, classified in class 257, subclass 371.
- II. Claims 1-13, drawn to a semiconductor structure formed by a method, classified in class 438, subclass 223.

The inventions are distinct, each from the other because of the following reasons: Inventions I and II are related as process of making and product made. The inventions are distinct if either or both of the following can be shown: (1) that the process as claimed can be used to make other and materially different product or (2) that the product as claimed can be made by another and materially different process (MPEP § 806.05(f)). Unpatentability of the Group I invention would not necessarily imply unpatentability of the Group II invention, since the device of the Group I invention could be made by a process materially different from those/that of the Group II invention. In the instant case, the semiconductor structure as claimed can be made by another and materially different process, such as performing the source and drain regions before depositing the gate electrode.

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Because these inventions are distinct for the reasons given above and have acquired a separate status in the art as shown by their different classification, restriction for examination purposes as indicated is proper.

Applicant is advised that the reply to this requirement to be complete must include an election of the invention to be examined even though the requirement be traversed (37 CFR 1.143).

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Ida M. Soward whose telephone number is 571-272-1845. The examiner can normally be reached on Monday - Thursday 6:30am to 5:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Amir Zarabian can be reached on 571-272-1852. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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